

Année	Auteurs	Titre		Sigle	Conférence	Lieu, date	Actes
2010	M.G. Pala	Elastic and inelastic scattering in SiNWs	Invited paper	Workshop	1st Ukrainian-French Symposium 'Semiconductor-On-Insulator Materials, Devices and Circuits: Physics, Technology and Diagnostics'	Kiev, Ukraine (25-29 Oct. 2010)	
2010	V. Dobrovolsky, F. Sizov, S. Cristoloveanu	Gate control of junction impact ionization avalanche in SOI MISFETs : theoretical model.	Invited paper	Workshop	1st Ukrainian-French Symposium 'Semiconductor-On-Insulator Materials, Devices and Circuits: Physics, Technology and Diagnostics'	Kiev, Ukraine (25-29 Oct. 2010)	
2010	K-I. Na, W. Van Den Daele, L. Pham-Nguyen, M. Bawedin, K-H. Park, J. Wan, K. Tacchi, S-J. Chang, I. Ionica, Y-H. Bae, J-A. Chroboczek, C. Fenouillet-Beranger, T. Ernst, E. Augendre, C. Le Royer, A. Zaslavsky, H. Iwai, S. Cristoloveanu	Selected SOI puzzles and tentative answers.	Invited paper	Workshop	1st Ukrainian-French Symposium 'Semiconductor-On-Insulator Materials, Devices and Circuits: Physics, Technology and Diagnostics'	Kiev, Ukraine (25-29 Oct. 2010)	
2010	G. Ghibaudo	Mobility characterization in advanced FD-SOI CMOS devices	Invited paper	Workshop	1st Ukrainian-French Symposium 'Semiconductor-On-Insulator Materials, Devices and Circuits: Physics, Technology and Diagnostics'	Kiev, Ukraine (April 2010)	
2010	F. Balestra	The Sinano Institute: European Networks and Projects in the fields of More Moore, More than Moore and Beyond-CMOS	Invited paper	Workshop	4th International Conference on Micro-Nanoelectronics, Nanotechnologies & MEMs	Athens, December 2010	
2010	F. Balestra	Silicon-based devices and materials for nanoscale FETs	Invited paper	Workshop	6th International SemOI Conference & 1st Ukrainian-French Seminar on SOI Materials, Devices and Circuits	Kiev, Ukraine (25-29 Oct. 2010)	

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2010	M. Bawedin, K-H. Park, K-I. Na, Y-H. Bae, S. Cristoloveanu	Floating body effects for SOI memories.	Invited paper	Workshop	6th International SemOI Workshop 'Nanoscaled Semiconductor-on- Insulator Materials, Sensors and Devices'	Kiev, Ukraine (25-29 Oct. 2010)	
2010	A. Nassiopoulou, F. Balestra	European Nanoelectronics: the Initiatives and Networks of the Academic Community	Invited paper	Workshop	Euro Nano Day	Grenoble, May 2010	
2010	G. Ghibaudo	Electrical Characterization of Si nanowires	Invited paper	Workshop	Int. Workshop on silicon nanowires (NANOSIL)	Louvain, Belgium (March 2010)	
2010	G. Ghibaudo, C. Mezzomo, A. Bajolet, A. Cathignol and R. DiFrenza	Characterization and modelling of device variability in advanced CMOS technologies	Invited paper	Workshop	Int. workshop on Simulation and Characterization of Statistical CMOS Variability and Reliability	Bologna, Italy (Sept 2010)	
2010	V. Andrieux, M. Auguste, D.Boyer, A. Cavaillou, C. Clarke, P. Febvre, S.Gaffet, S. Henry, H. Kraus, A. Lynch, V. Mikhailik, M. McCann, E. Pozzo di Borgo, C. Sudre, G. Wavsand	Characterisation of magnetic field fluctuations at different locations within the Laboratoire Souterrain à Bas Bruit using a new SQUID prototype		Workshop	Interdisciplinary Underground Science and Technology Workshop	Apt, France (8-11 June 2010)	Published online by EDP Sciences (www.i-dust.org) doi: 10.1051/idust/201102003
2010	V. Andrieux, M. Auguste, D.Boyer, A. Cavaillou, C. Clarke, P. Febvre, S.Gaffet, S. Henry, H. Kraus, A. Lynch, V. Mikhailik, M. McCann, E. Pozzo di Borgo, C. Sudre, G. Wavsand	Characterisation of magnetic field fluctuations at different locations within the Laboratoire Souterrain à Bas Bruit using a new SQUID prototype		Workshop	Interdisciplinary Underground Science and Technology Workshop (iDUST)	Apt, France (8-11 June 2010)	Published online by EDP Sciences (www.i-dust.org) doi: 10.1051/idust/201102003
2010	T. Benoist, C. Fenouillet-Beranger, P. Galy, C. Buj, O. Faynot, P. Perreau, B. Jaquier, P. Gentil	Experimental study of gated diode as ESD protection in FDSOI - IEW (International ESD Workshop), poster, mai 2010, Tutzig, Germany.	Poster	Workshop	International Electrostatic Discharge Workshop (IEW)	Tutzig, Germany (May 2010)	CD workshop
2010	F. Balestra	European Research Roadmap for Nanoelectronics	Invited paper	Workshop	Minatec Crossroads	Grenoble, June 2010	

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2010	R. Ritzenthaler, F. Lime, O. Faynot, S. Cristoloveanu, B. Iniguez	Analytical modelling of short channel planar FDSOI and triple-gateFET transistors.		Workshop	MOS-AK/GSA Workshop 'Frontiers of the Compact Modeling for Advanced Analog/ RF Applications', 40th European Solid-State Device Research Conference (ESSDERC'10)	Seville, Spain (17 Sept. 2010)	
2010	Pascal Febvre, David Bouis, Natascia De Leo, Matteo Fretto, Andrea Sosso, Vincenzo Lacquaniti	Evaluation of self-shunted SNIS Josephson junctions for RSFQ applications		Workshop	Savoie workshop on Superconducting Electronics	Chambéry, May 27-28, 2010	CD workshop
2010	M. Mouis, L. Montés, X. Xu, J.W. Lee, F. Rochette, R. Hinchet, G. Ardila	Nanowires in the Beyond CMOS and More than Moore perspectives : Electromechanical properties	Invited paper	Workshop	SINANO-NANOSIL Workshop: "On the convergence between More Moore, More Than Moore and Beyond CMOS"	Seville, Spain (17 Sep. 2010)	
2010	S. Cristoloveanu, M. Bawedin, N. Rodriguez, K-H. Park, A. Hubert, F. Gamiz	New concepts for 1T-DRAMs on SOI.		Workshop	Workshop on Innovative Memory Technology, MINATEC Crossroads'10	Grenoble, France (21-24 juin 2010)	
2010	S. Cristoloveanu	1T-DRAM structures.	Invited paper	Tutorial	Tutorial 'Nanoelectronics: a tool to face the future', 40th European Solid-State Device Research Conference (ESSDERC'10)	Seville, Spain (13 Sept. 2010)	
2010	T. Benoist, C. Fenouillet-Beranger, N. Guitard, J.-L. Huguenin, S. Monfray, P. Galy, C. Buj, O. Faynot, F. Andrieu, P. Perreau, D. Marin-Cudraz, S. Cristoloveanu, P. Gentil	Improved ESD Protection in Advanced FDSOI by SON/Bulk Co-integration - EOS/ESD symposium 2010, september, Anaheim, CA USA		Symposium	34th Annual EOS/ESD Symposium (EOS/ESD Symposium)	Anaheim, CA, USA (Sept. 2010)	
2010	S. Cristoloveanu	Advanced SOI and multi-gate transistors : special mechanisms and characterization.	Invited paper	International School	1st International Training Courses on Compact Modelling	Tarragona, Spain (30 juin - 1 july 2010)	
2010	S. Cristoloveanu	Characterization techniques for SOI materials and transistors.	Invited paper	International School	1st Korean International Summer School on Nanoelectronics (nano-KISS 2010) SOI Technology : Materials, Devices, and Applications	Daegu, Korea (7-10 april 2010)	

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2010	M. Clavel, T. Poiroux, L. Becerra, M. Mouis	Integration of a Graphene FET on SiC Substrates	Poster	International School	GDRI Graphene & Nanotubes, Graphene International School	Cargese, (11-23 Octobre 2010)	